

FIG.1

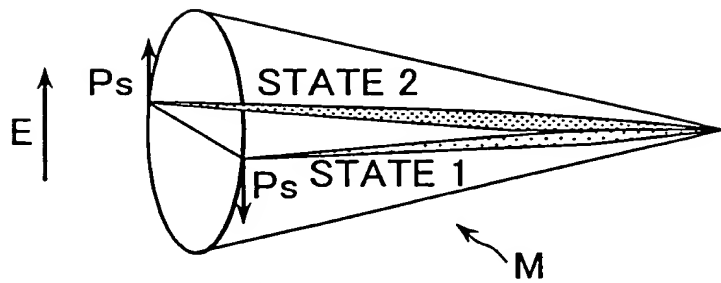


FIG.2

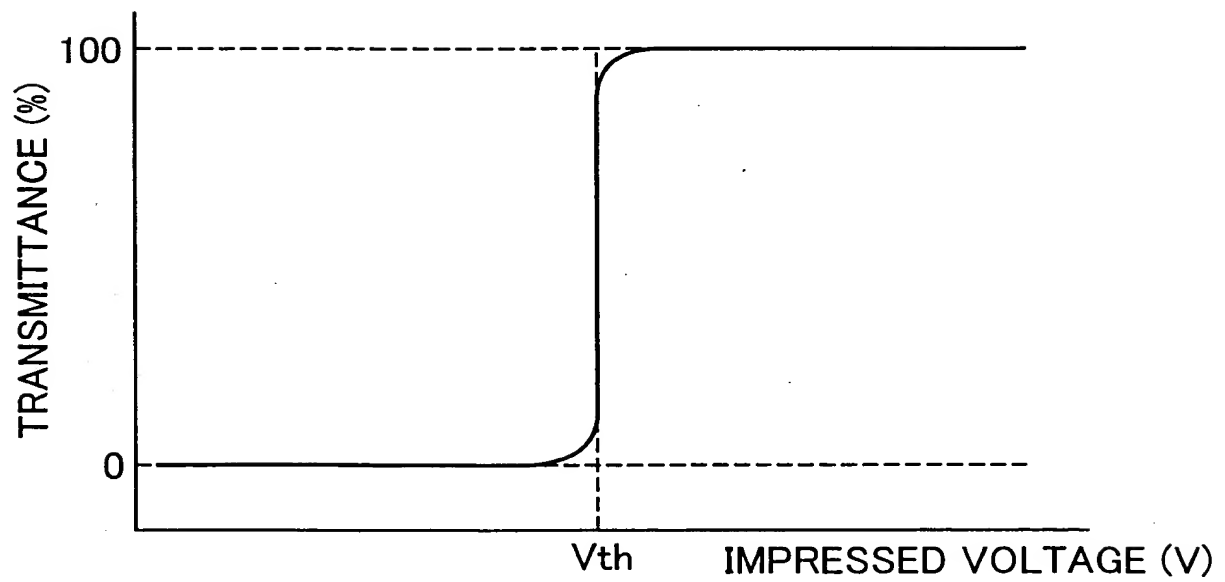


FIG.3

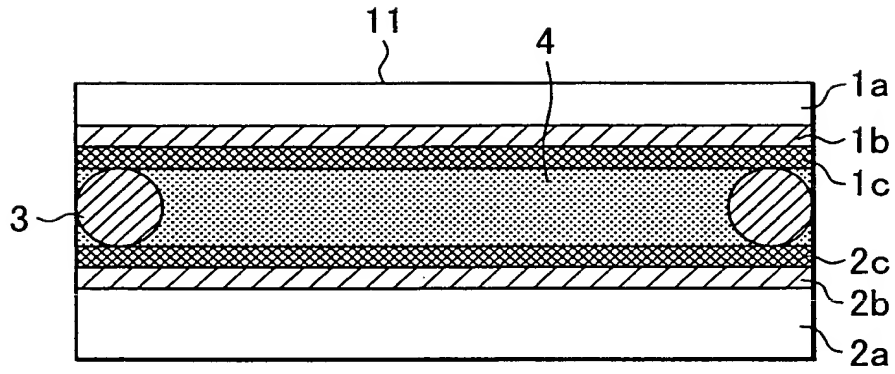


FIG. 4

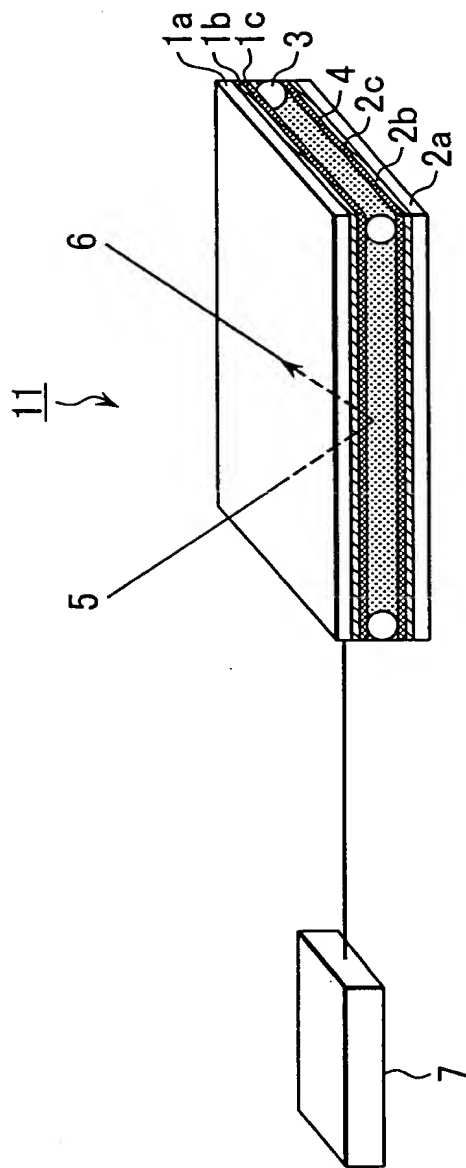




FIG.6

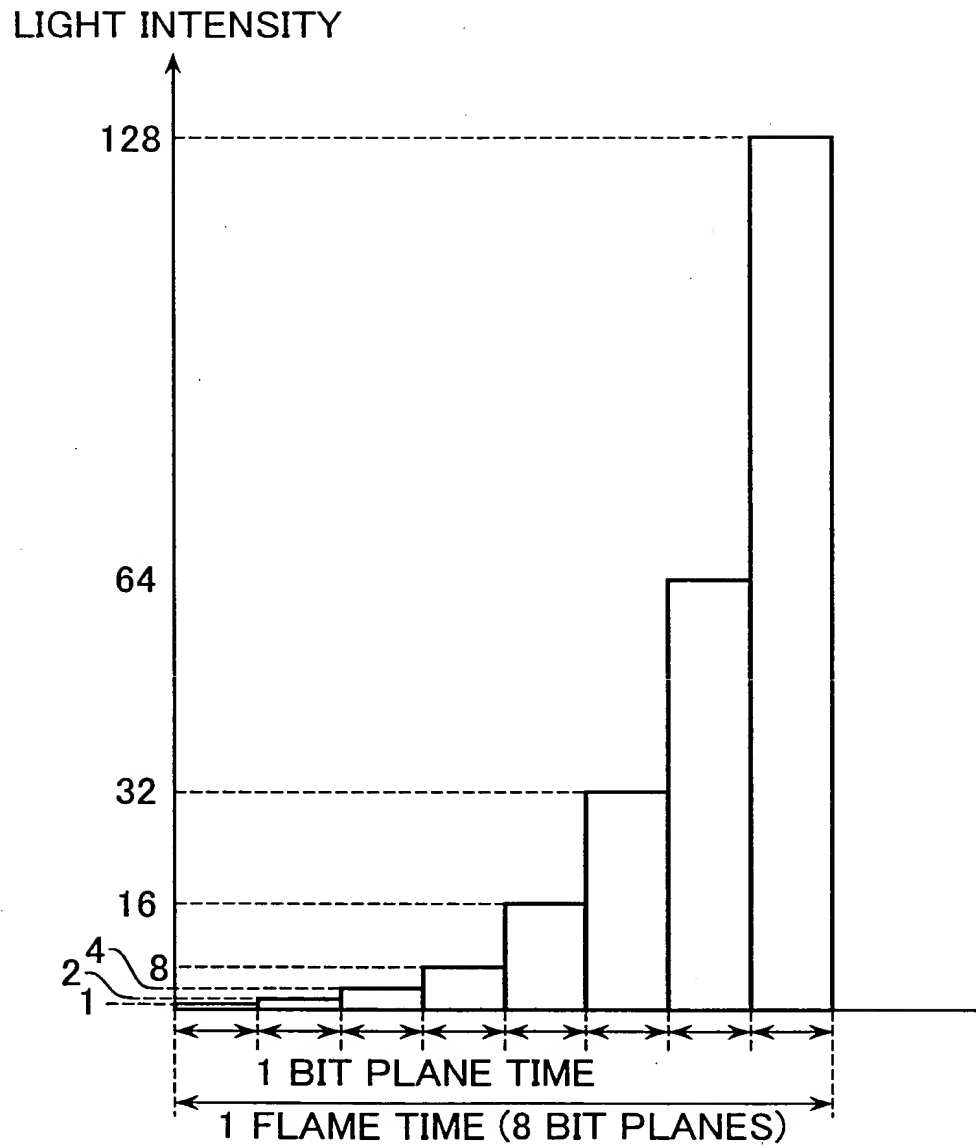
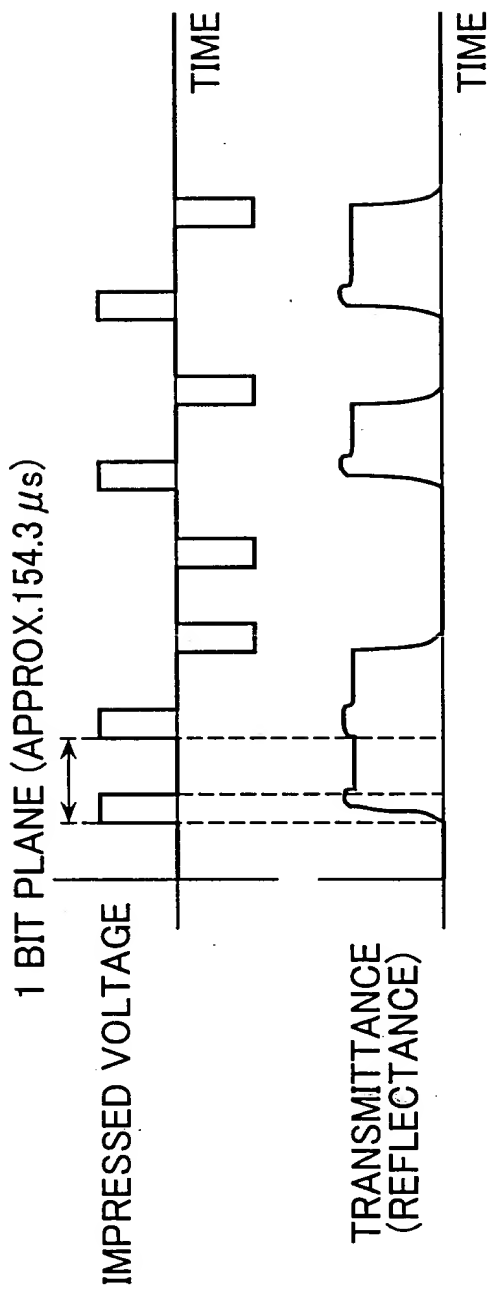


FIG.7



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FIG.8

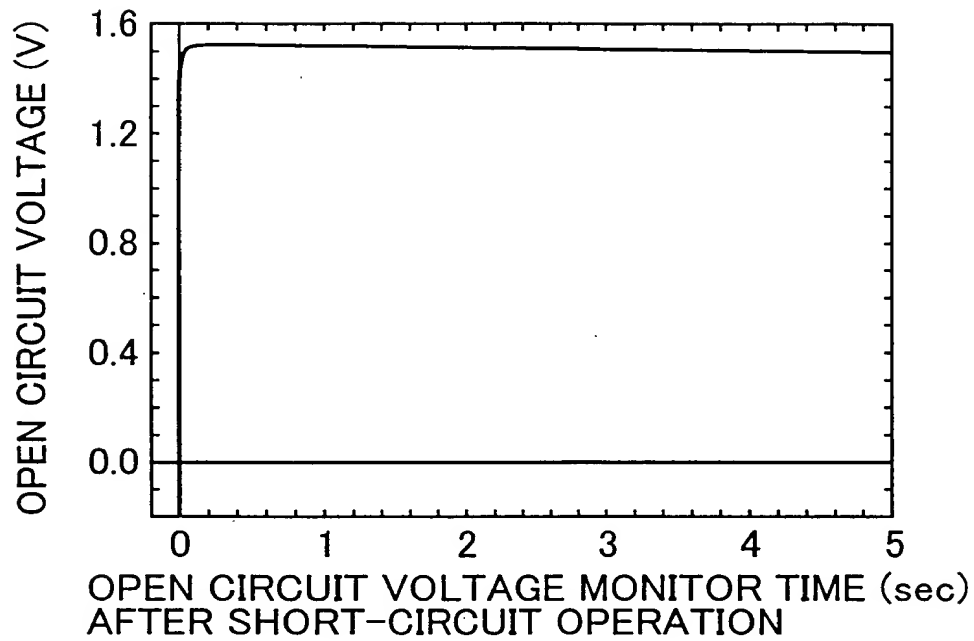
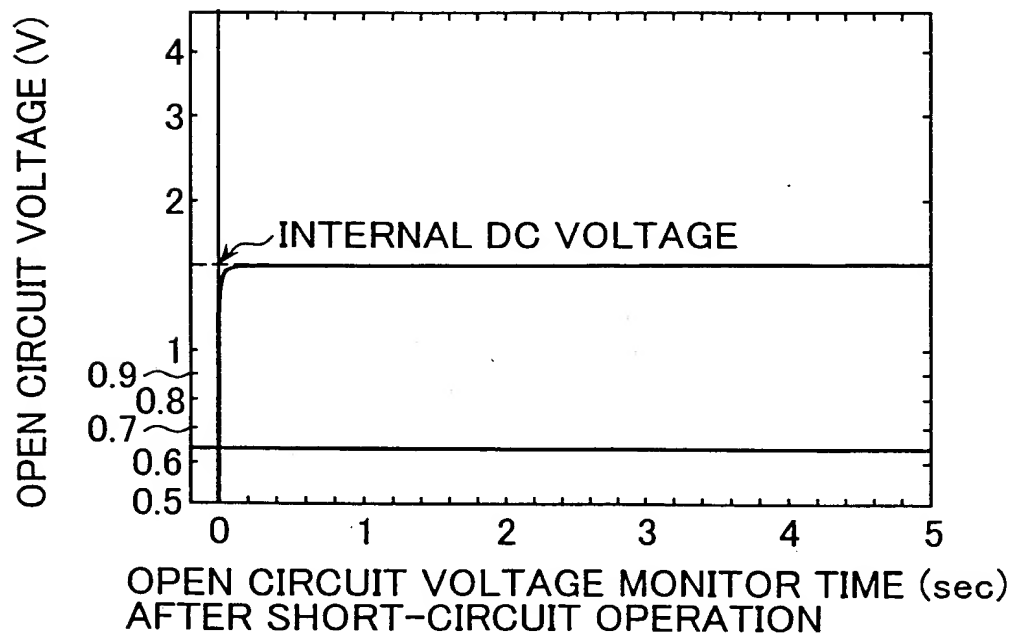


FIG.9



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FIG.10

(a) SHORT-CIRCUIT OPERATION DONE AFTER IMPRESSION OF NORMAL POSITIVE VOLTAGE WAVEFORM

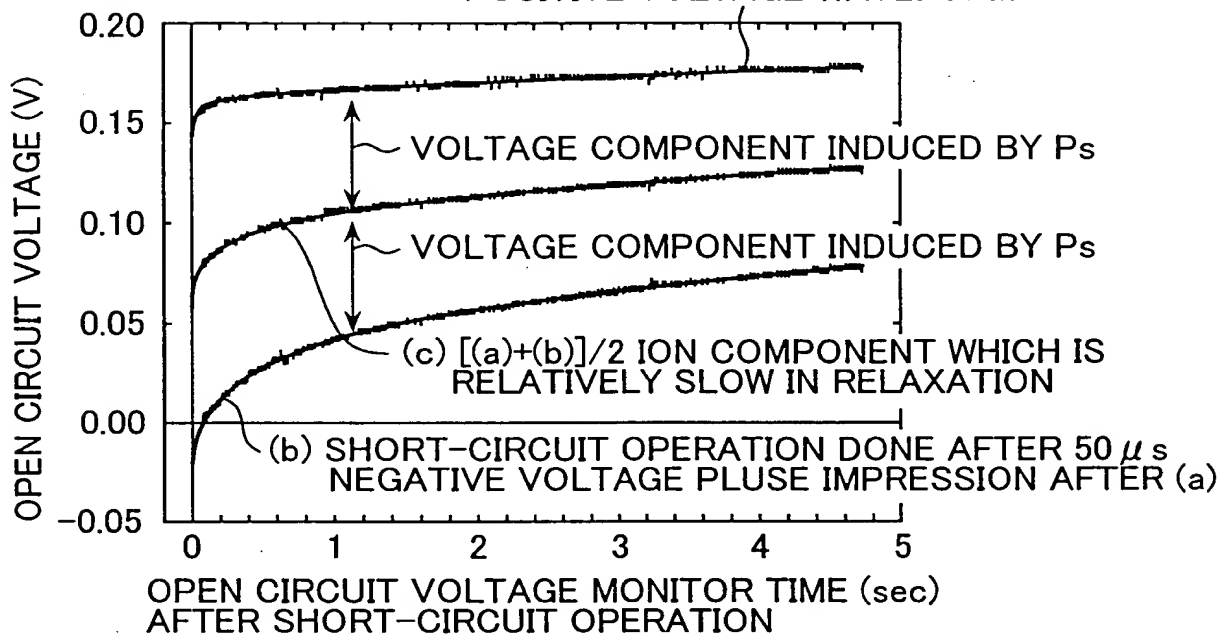
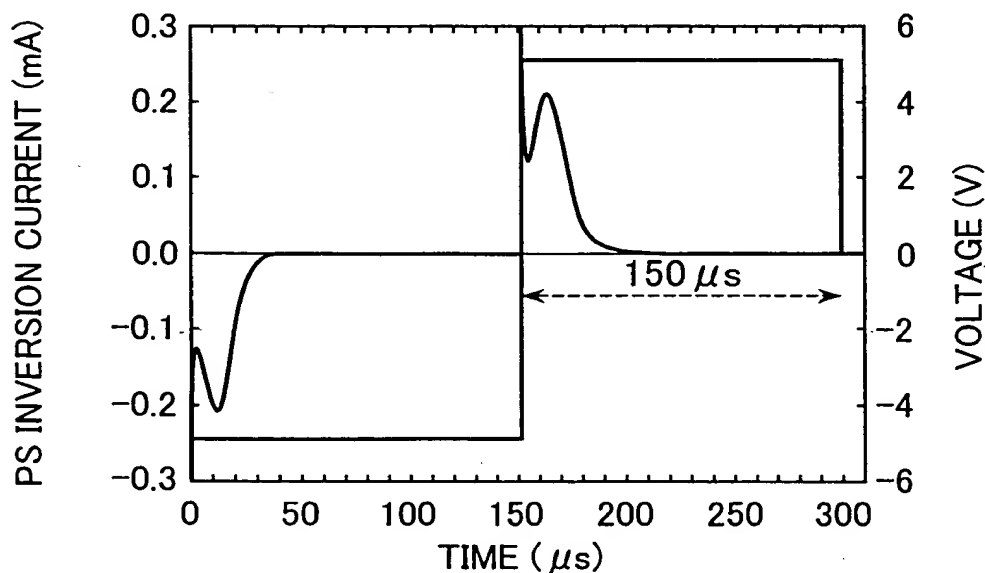


FIG.11





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FIG.12

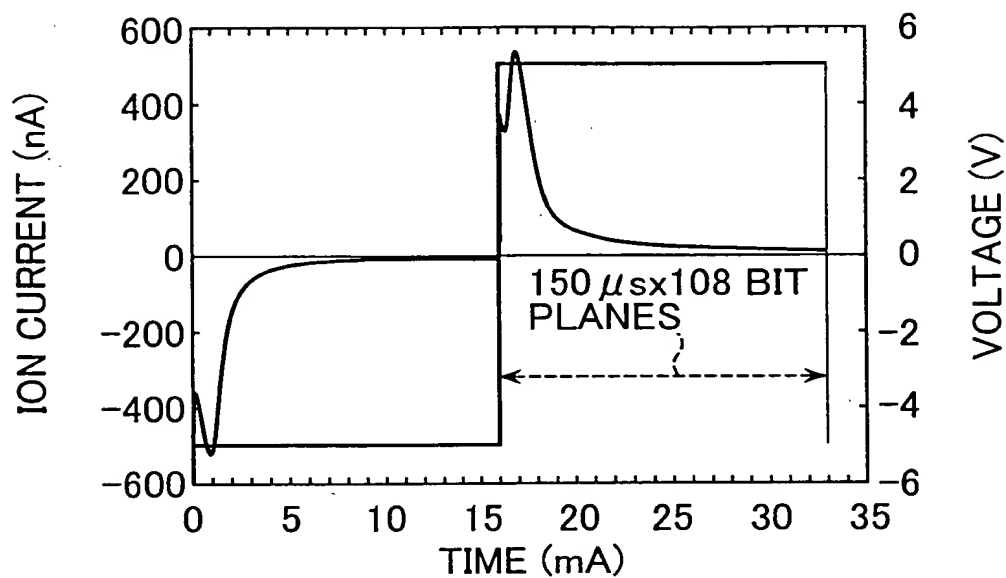
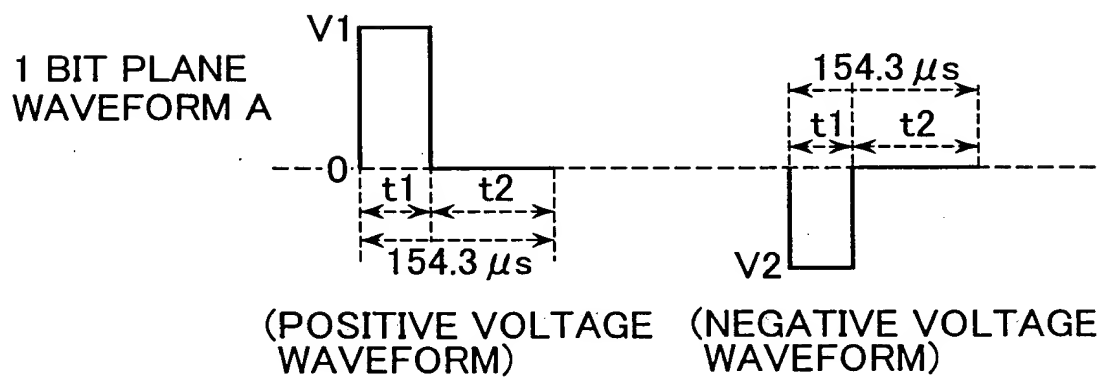


FIG.13



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FIG.14

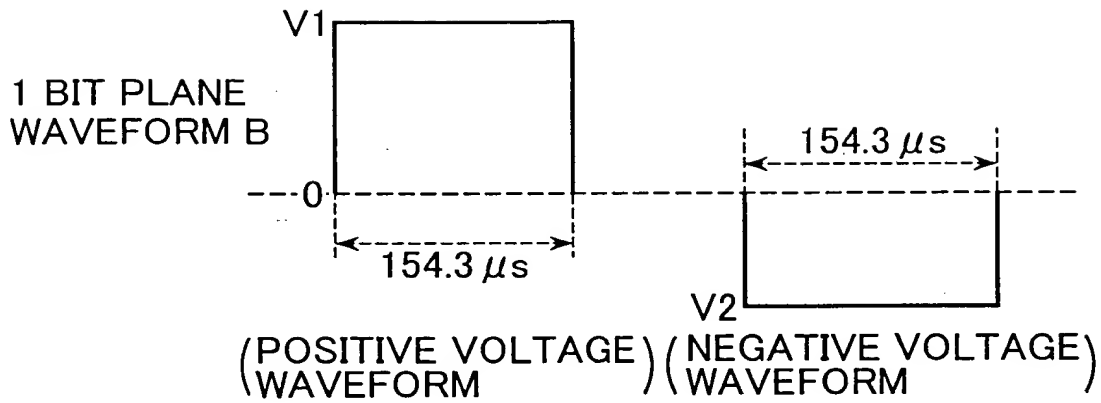


FIG.15

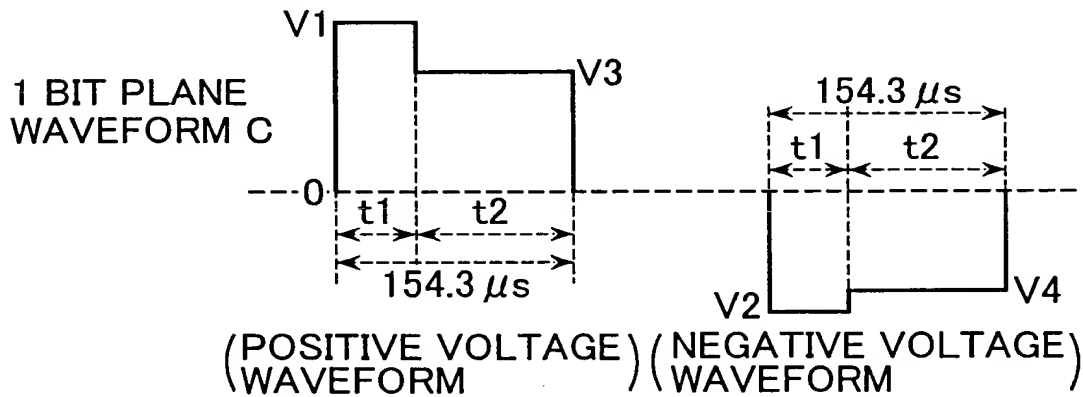
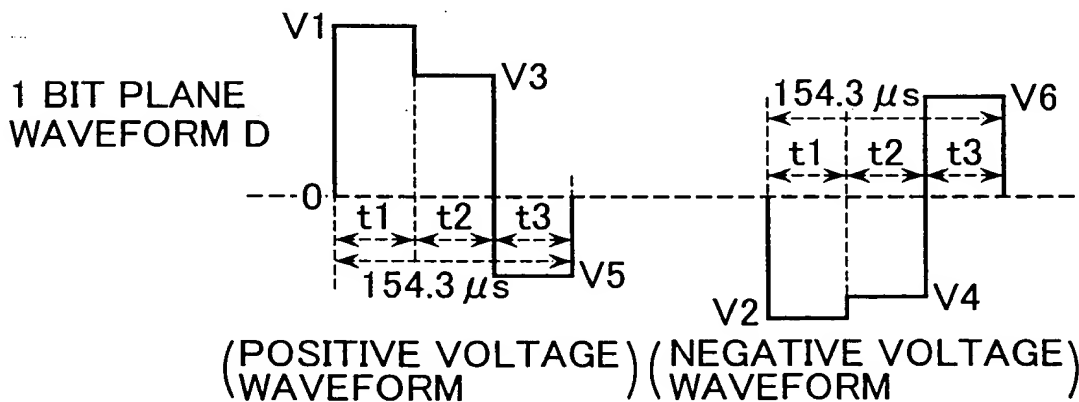


FIG.16



106250" 42729860

FIG.17

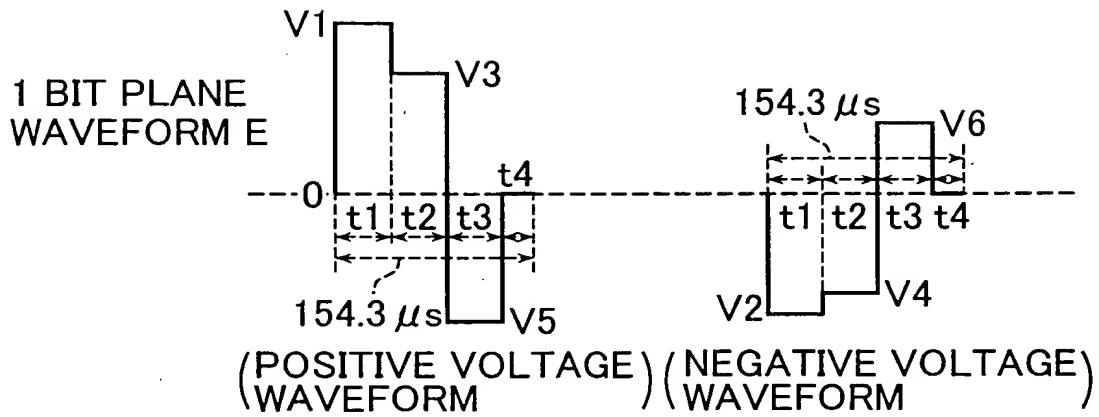


FIG.18

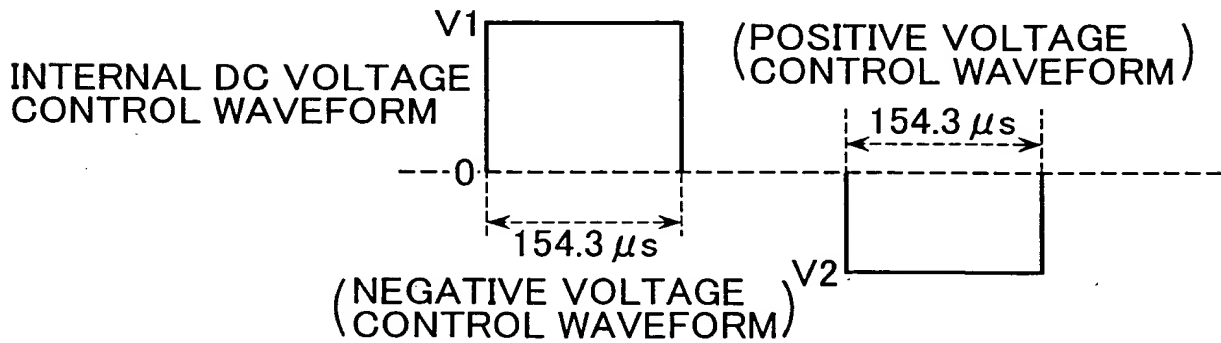
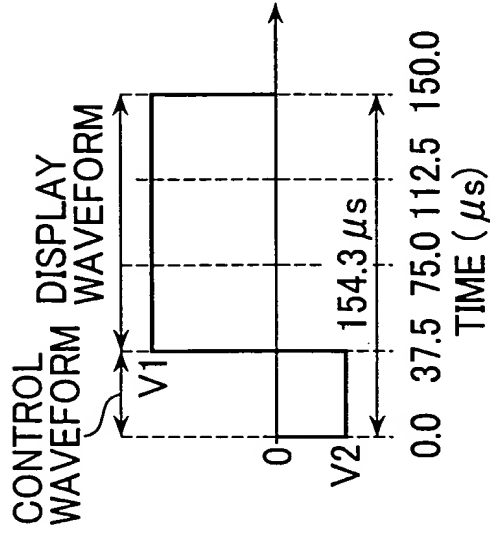


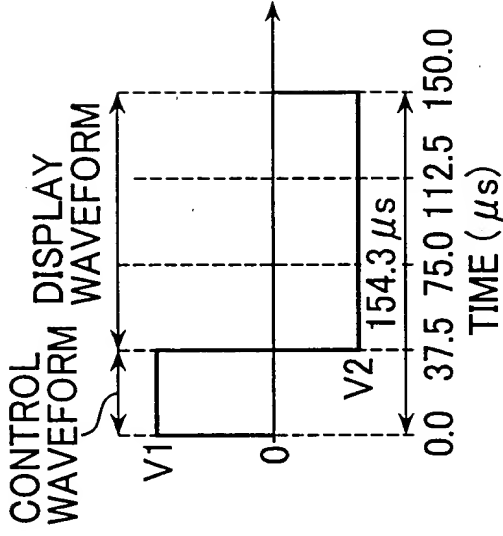
FIG.19A

$V_2=V_4$ ,  $V_1=V_3$ ,  $t_1=t_2=37.5 \mu s$ ,  $t_3=112.5 \mu s$



1 BIT PLANE WAVEFORM FOR DISPLAY  
WITH POSITIVE VOLTAGE

FIG.19B



1 BIT PLANE WAVEFORM FOR DISPLAY  
WITH NEGATIVE VOLTAGE

FIG.20

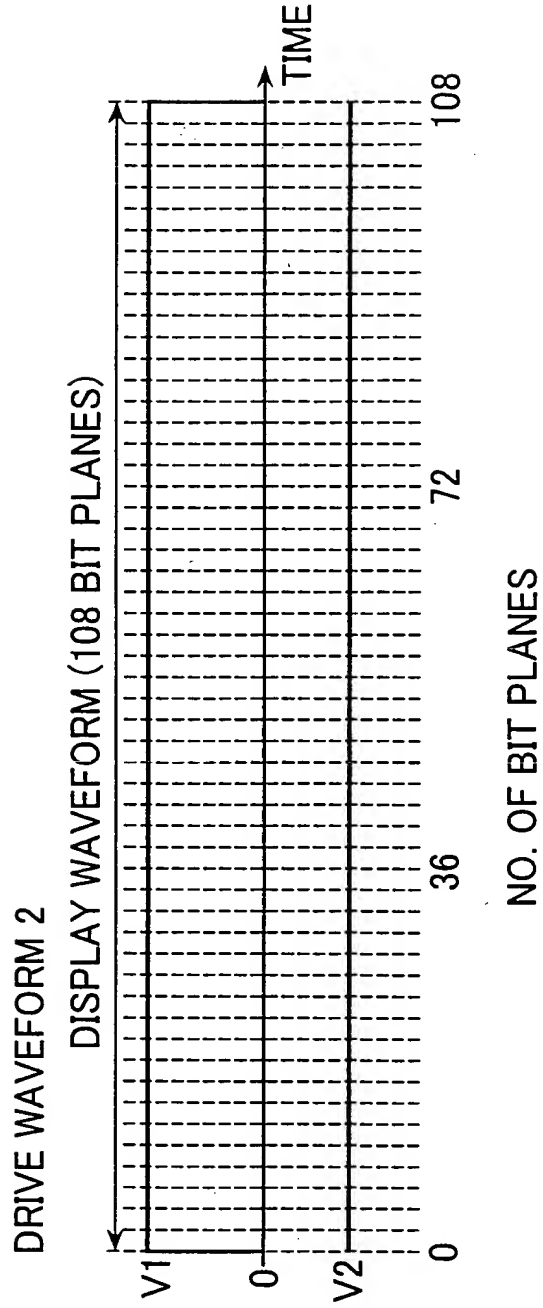


FIG.21

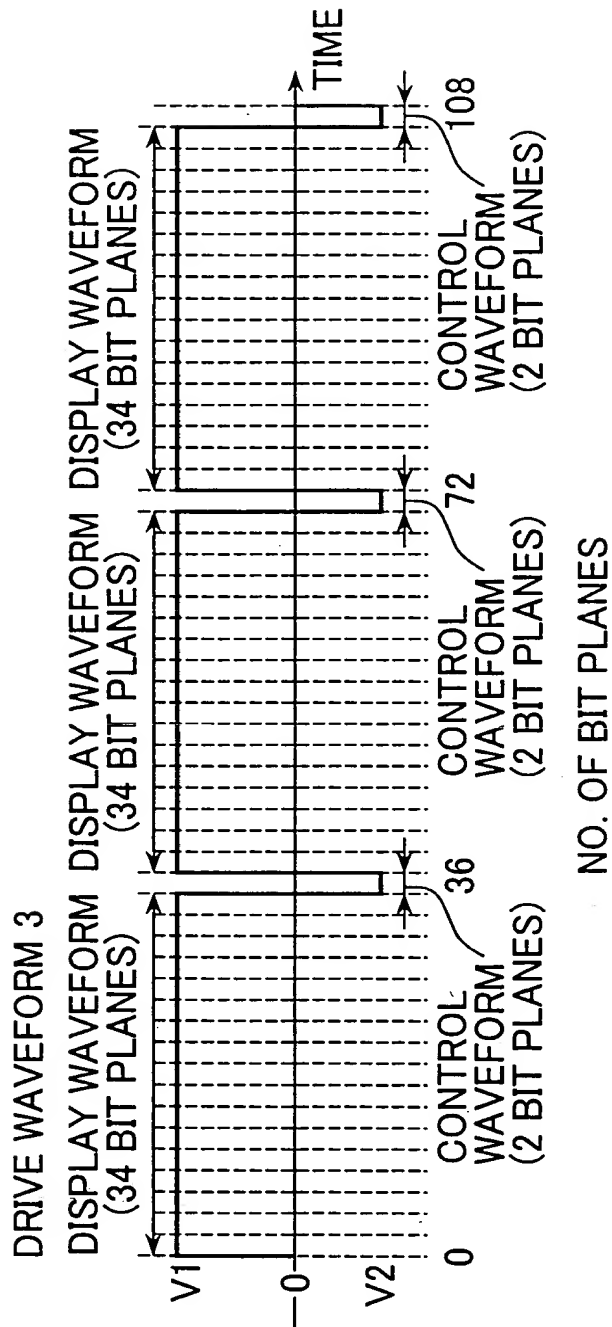
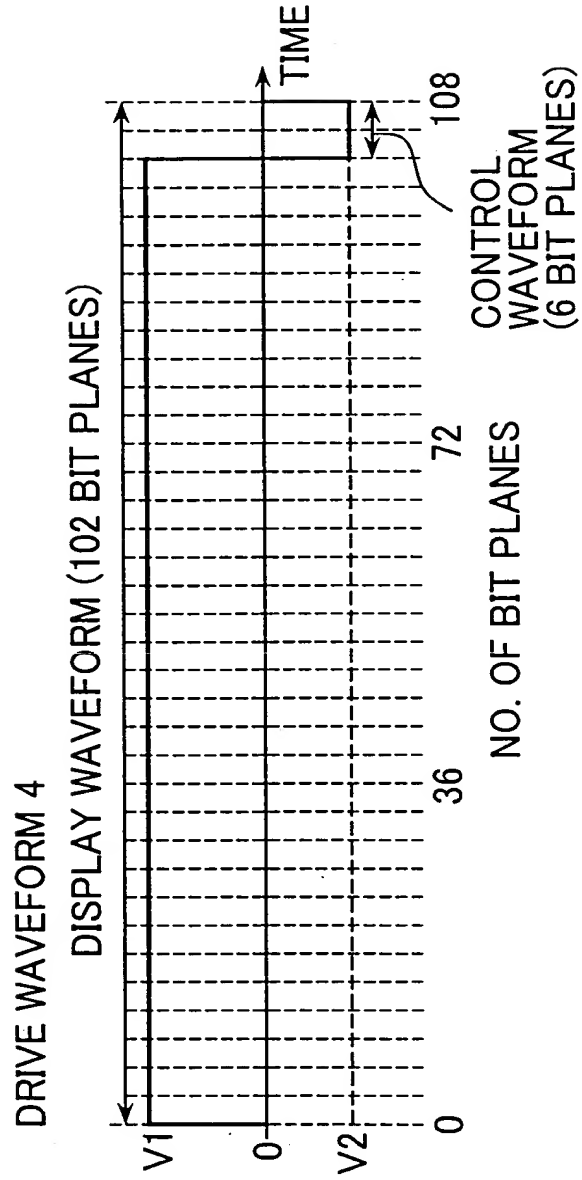
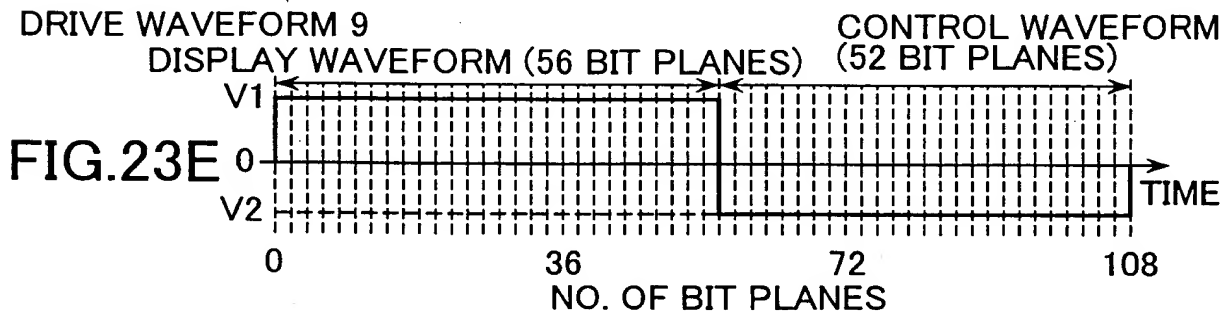
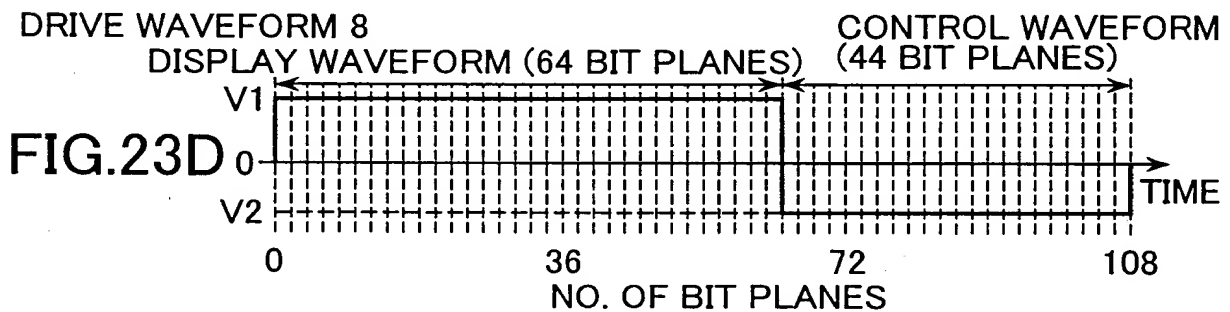
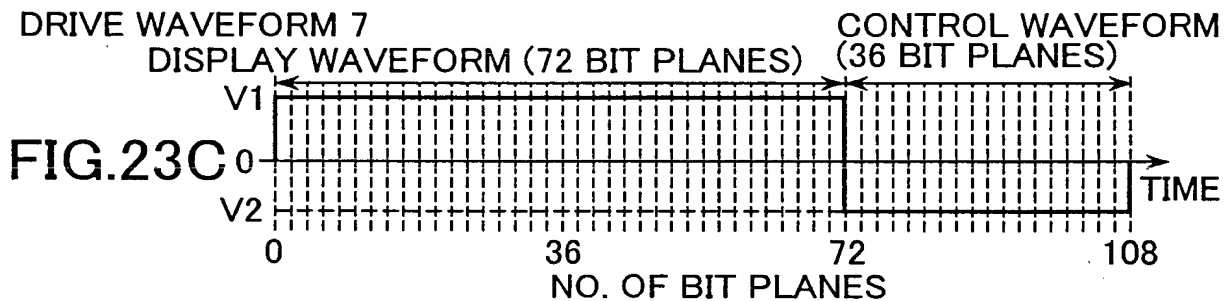
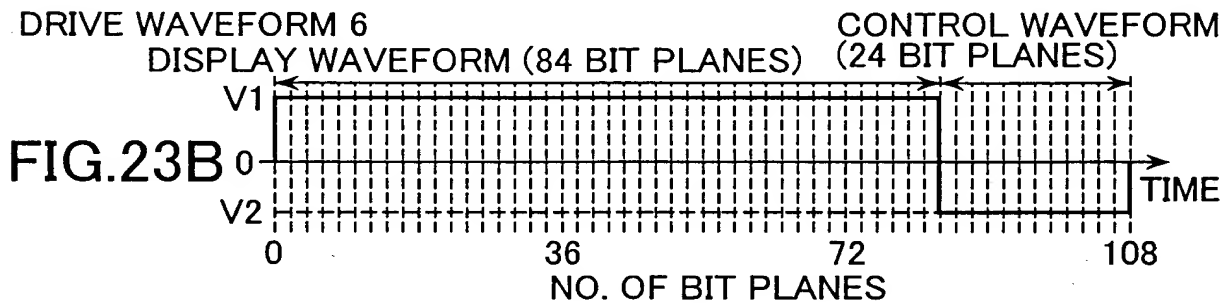
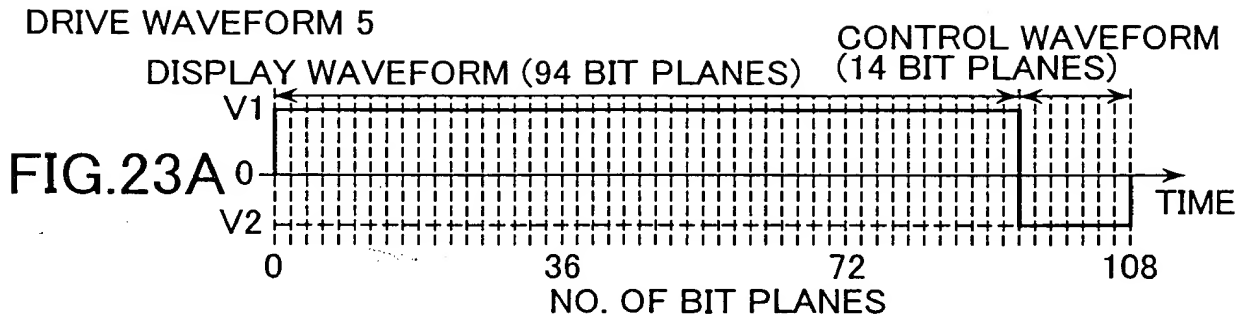
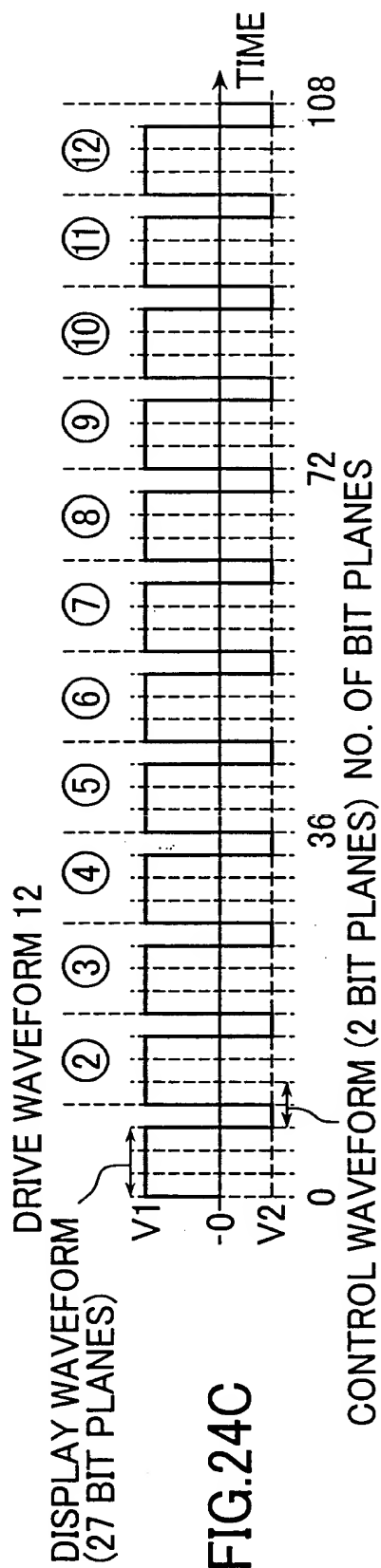
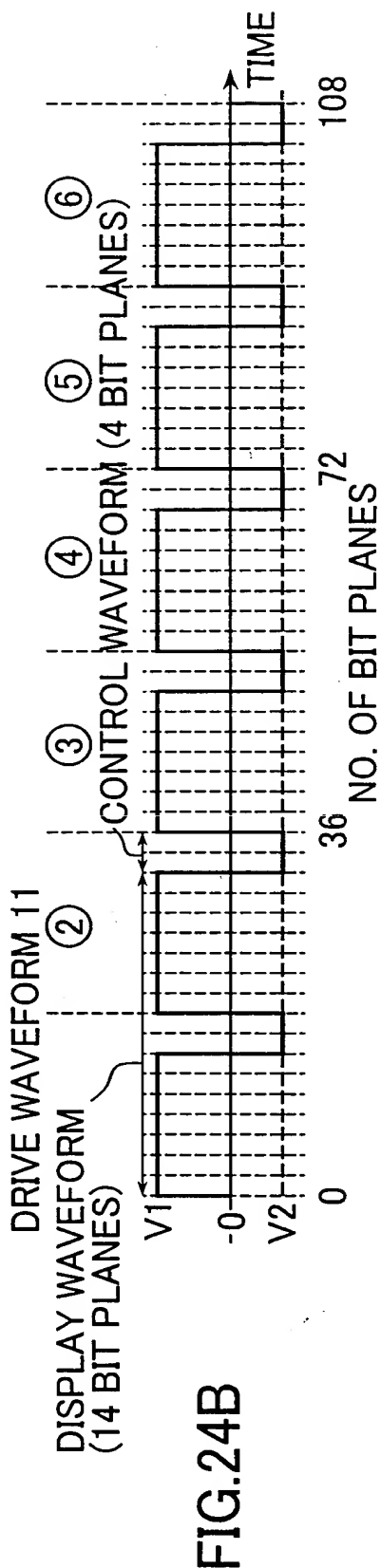
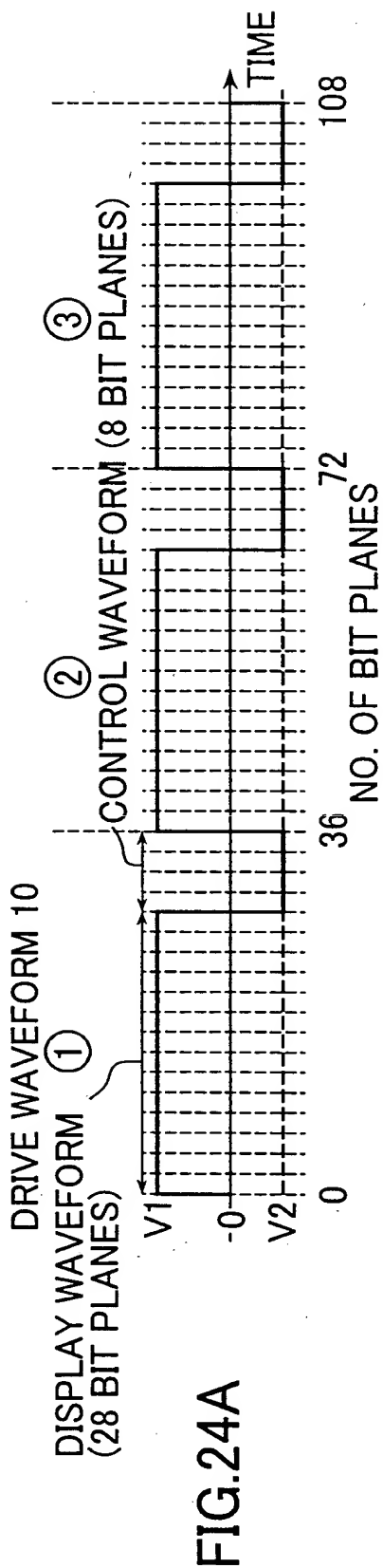


FIG.22

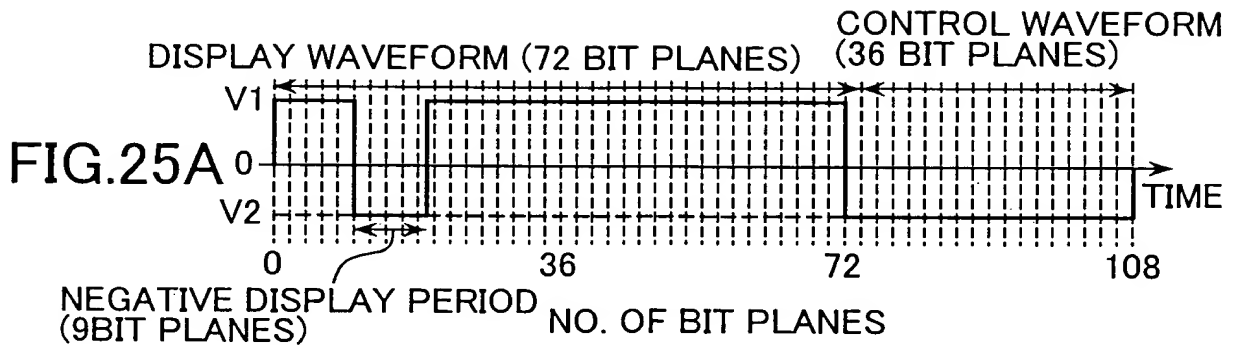




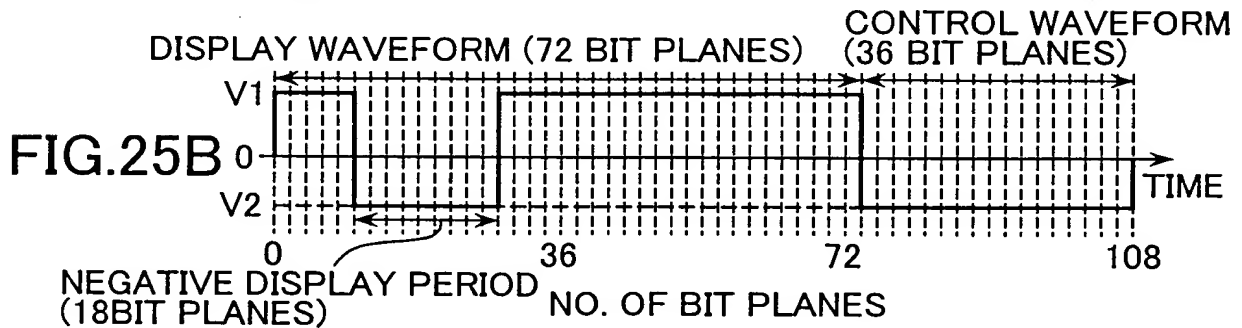




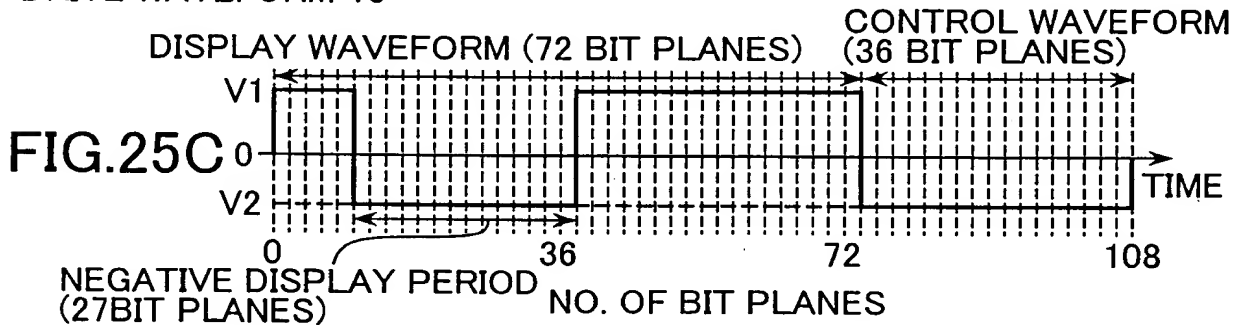
## DRIVE WAVEFORM 13



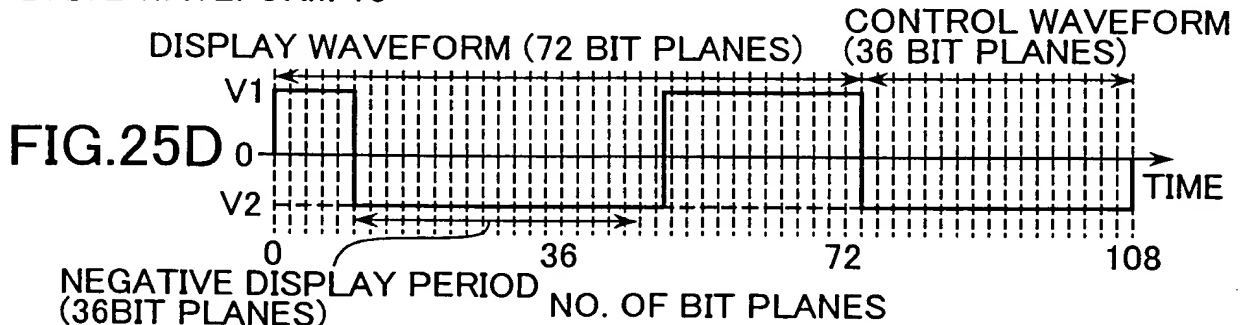
## DRIVE WAVEFORM 14



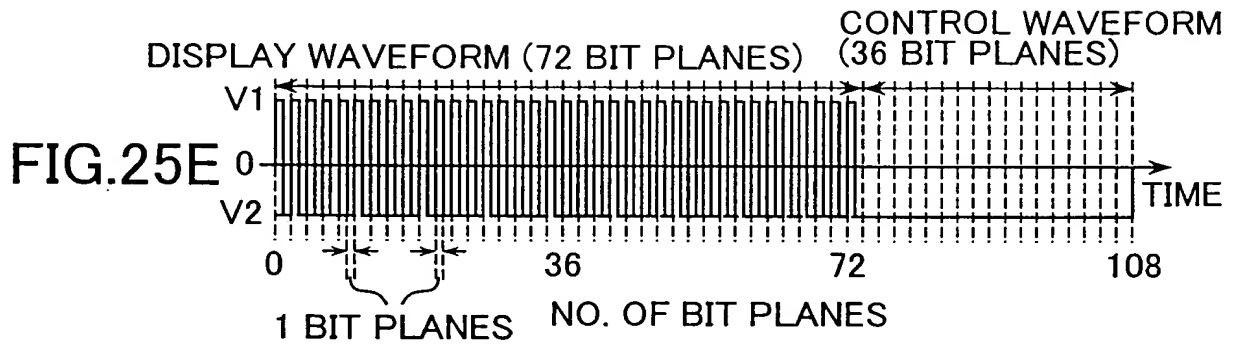
## DRIVE WAVEFORM 15



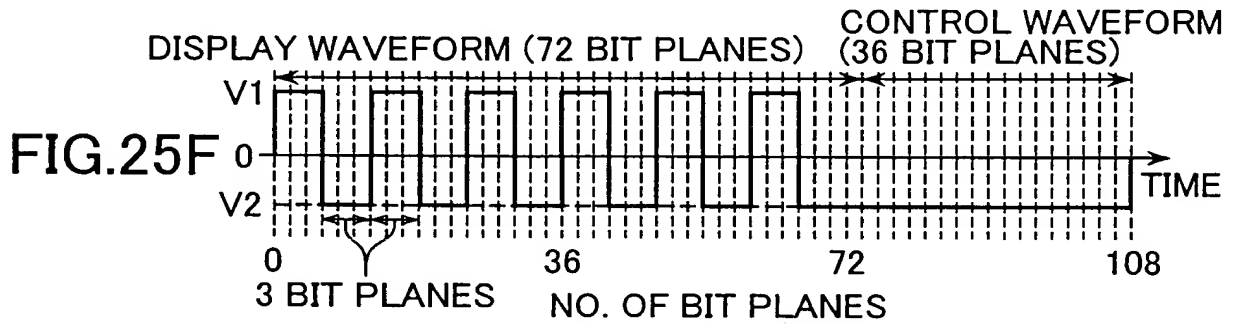
## DRIVE WAVEFORM 16



DRIVE WAVEFORM 17



DRIVE WAVEFORM 18



DRIVE WAVEFORM 19

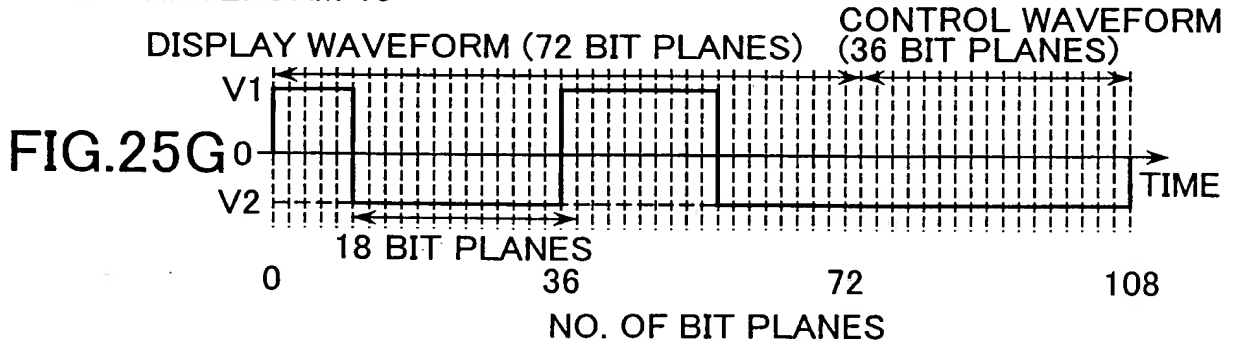
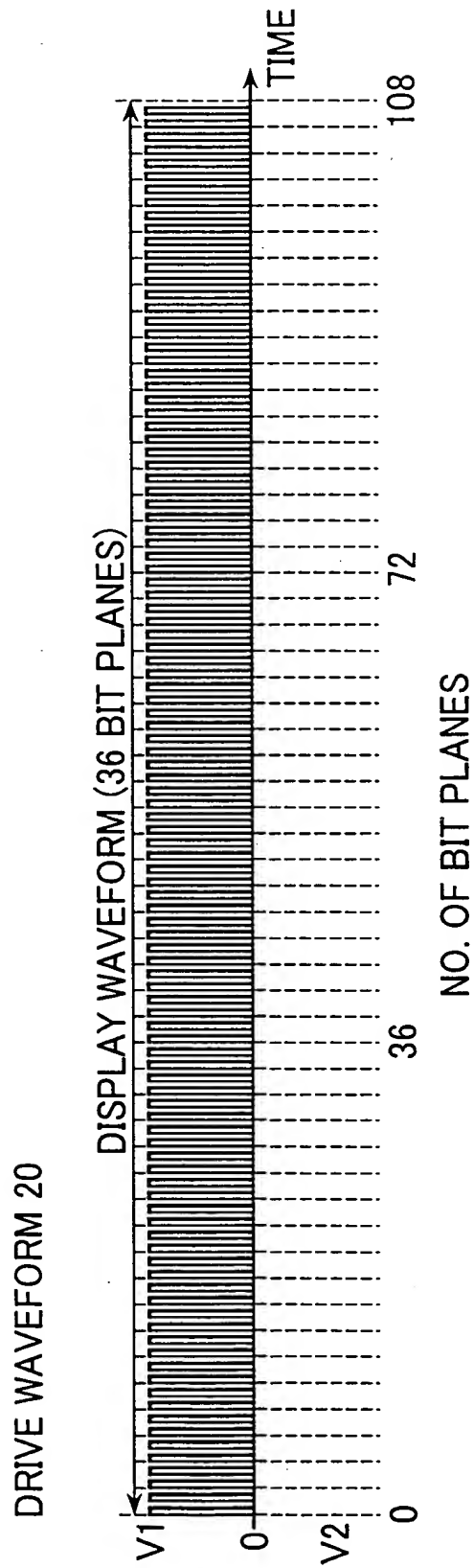


FIG.26

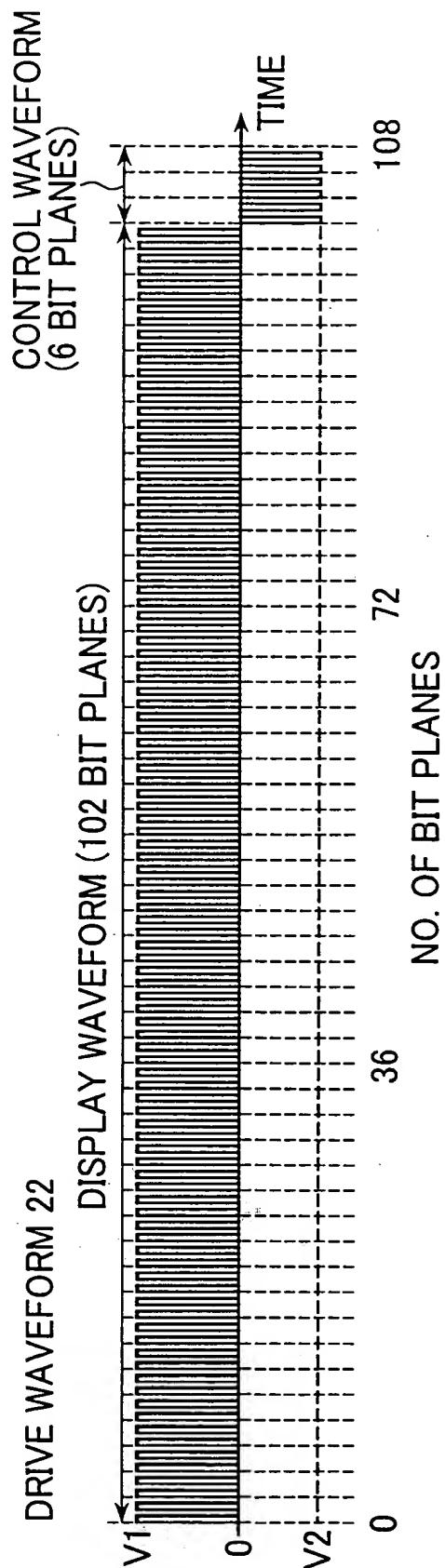


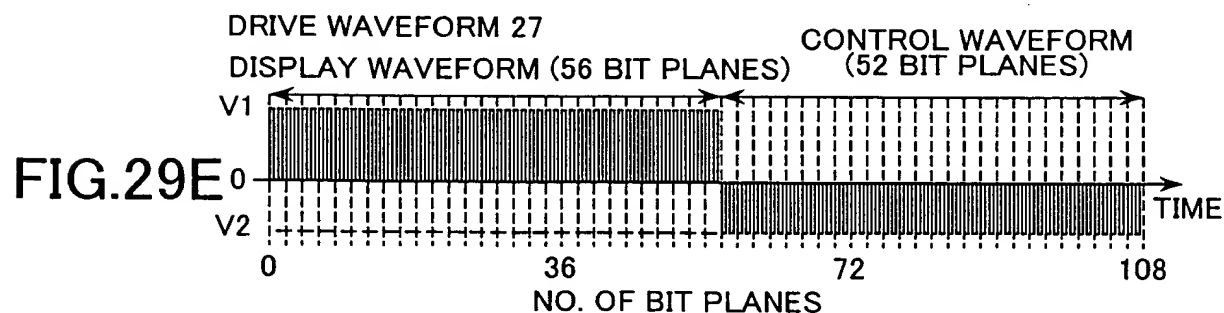
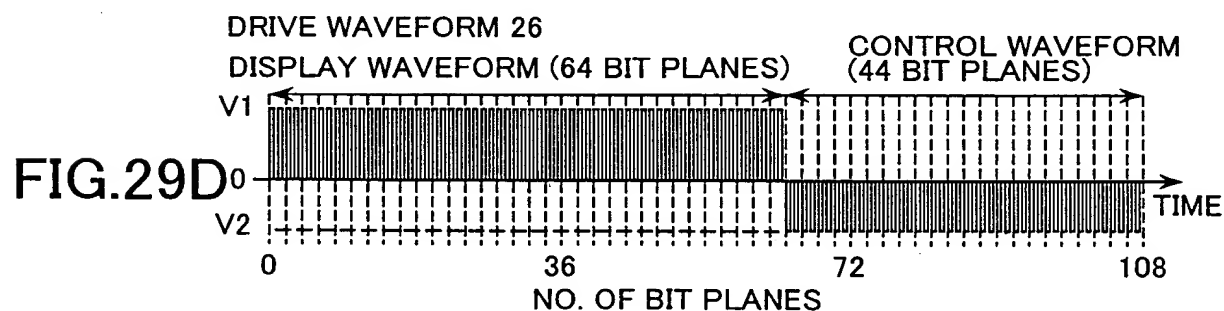
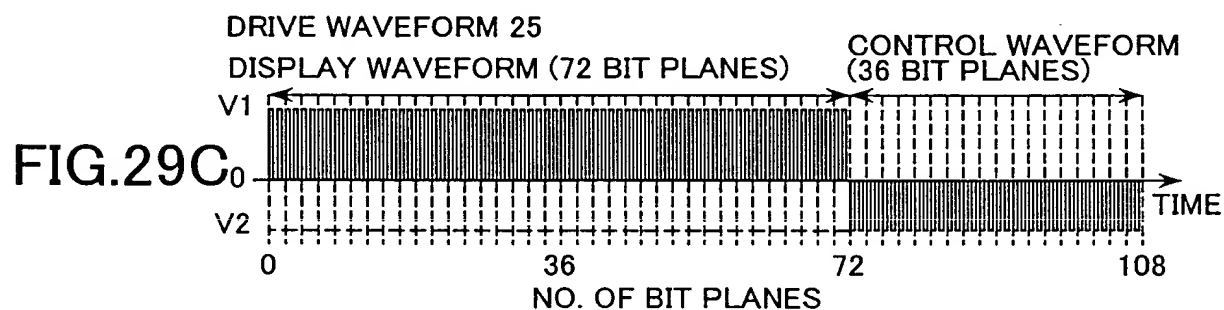
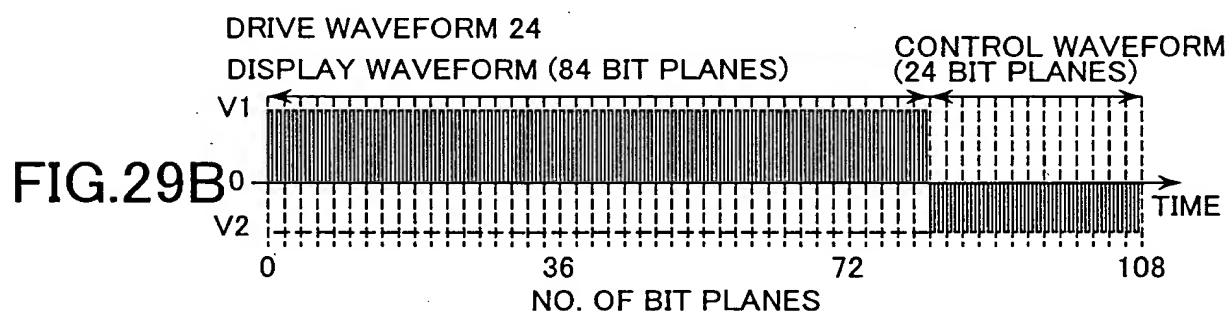
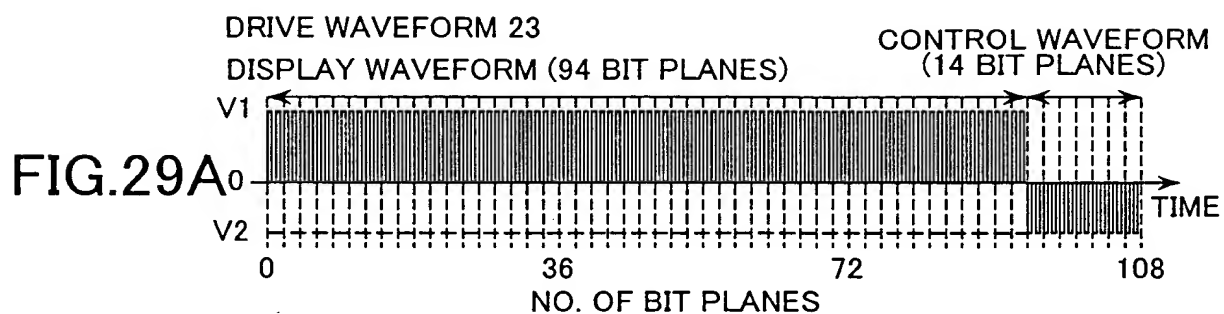
The diagram illustrates the timing for Drive Waveform 21. It features a horizontal axis labeled 'TIME' and a vertical axis with markers 'V1', '0', and 'V2'. The waveforms are organized into three main sections, each with a bracket indicating the number of bit planes:

- Section 1 (Left):** Labeled '36' and 'CONTROL WAVEFORM (2 BIT PLANES)'. It shows a control waveform pulse.
- Section 2 (Middle):** Labeled '72' and 'CONTROL WAVEFORM (2 BIT PLANES)'. It shows a control waveform pulse.
- Section 3 (Right):** Labeled '108' and 'CONTROL WAVEFORM (2 BIT PLANES)'. It shows a control waveform pulse.

Each of these three sections is preceded by a 'DISPLAY WAVEFORM (34 BIT PLANES)', which is represented by a dense series of vertical lines. The total duration of the display waveforms is indicated by a bracket labeled '34 BIT PLANES'.

FIG.28





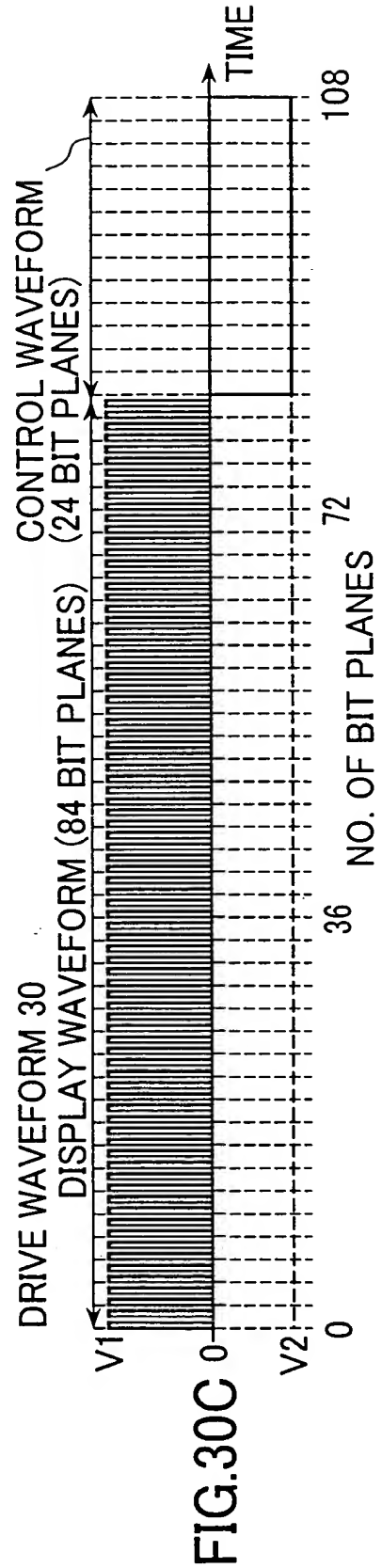
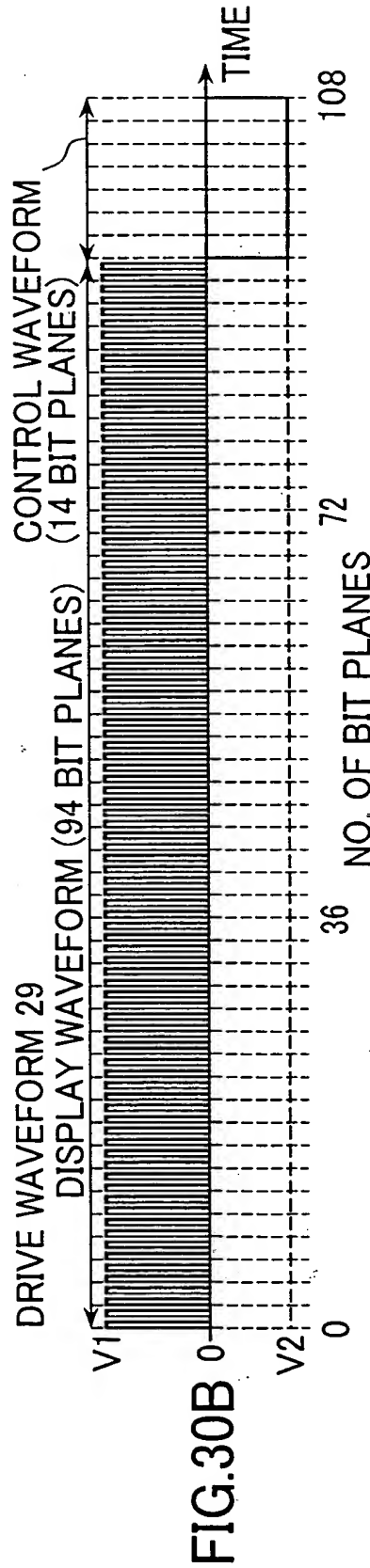
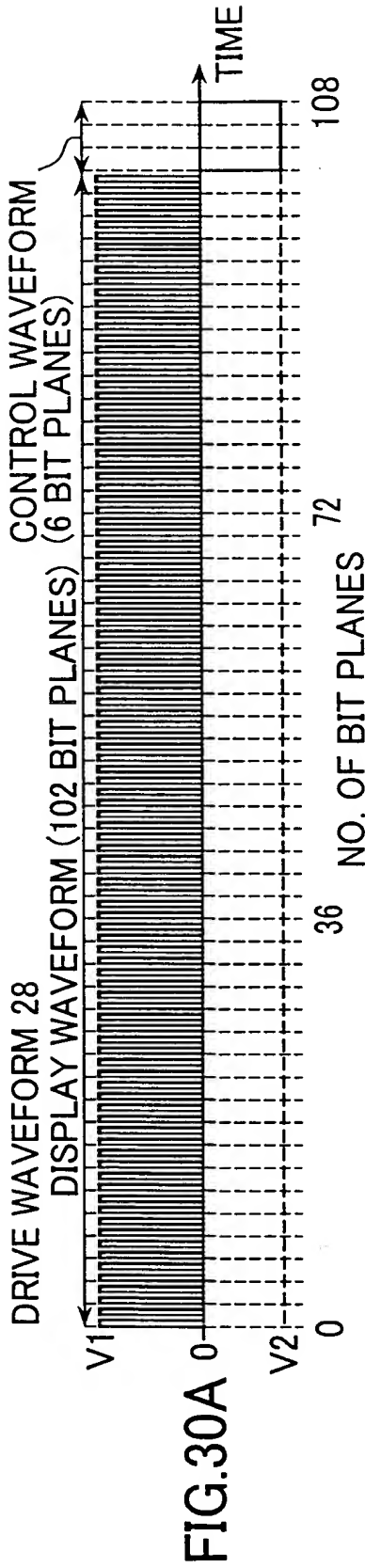




FIG.31

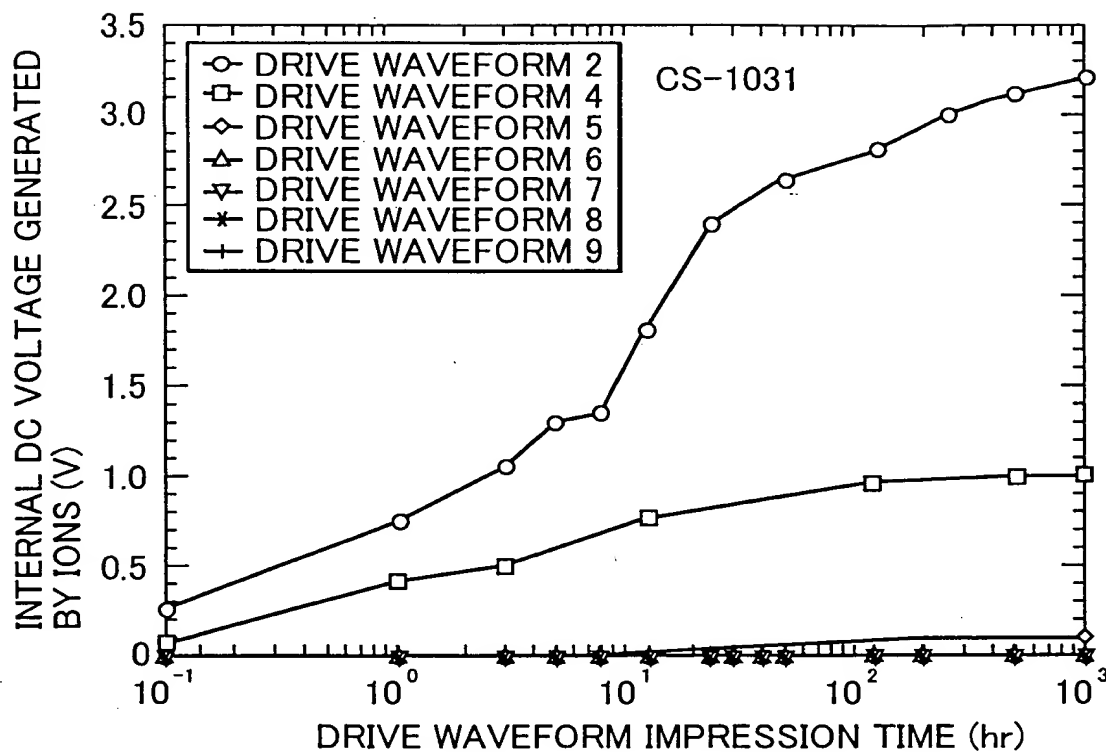


FIG.32

